

Dr. Howard Chen

Electrical engineer and computer scientist

National Council representative, Chinese Institute of Engineers, U.S.A.

Past President, Chinese Institute of Engineers, Greater New York Chapter

2015 Dynamic Achiever Award Recipient



Dr. Howard Chen received his B.S. degree in electrical engineering from the National Taiwan University in 1979, and Ph.D. degree in electrical engineering and computer sciences from the University of California, Berkeley in 1987. From 1987 to 2013, Dr. Chen worked at the IBM Research Division, Thomas J. Watson Research Center, in Yorktown Heights, New York, where he had been involved in the design and implementation of many IBM eServer products, including S/390, AS/400, PowerPC, pSeries, and zSeries. He also worked with IBM clients and participated in the design of Apple Power Mac, Nintendo GameCube, Microsoft Xbox, and Sony PlayStation processors. As a prolific inventor holding 37 U.S. patents, Dr. Chen has received numerous IBM Invention Achievement Awards for his creative contributions to IBM progress. He also received the IBM Outstanding Contribution Award for design and realization of the Alliance G5 microprocessor, the IBM Research Division Award for design and implementation of Freeway G7 microprocessor, and the Outstanding Technical Achievement Award at IBM's Corporate Technical Recognition Event.

Best known for his pioneering work on chip and package power supply noise analysis, Dr. Chen has published over 60 technical papers in professional journals and the proceedings of international conferences. He has also given tutorials and workshops at the Design Automation Conference (DAC), the Asia and South Pacific Design Automation Conference (ASP-DAC), the European Solid-State Circuits Conference (ESSIRC), and the International Solid-State Circuits Conference (ISSCC). From 2010 to 2013, Dr. Chen served as a member of the board of directors of the Chinese Institute of Engineers, Greater New York Chapter (CIE-GNYC). He was the program chair of CIE-GNYC convention in 2010, the treasurer of CIE-GNYC in 2011 and 2012, and the president of CIE-GNYC in 2013. Currently, Dr. Chen is a CIE National Council representative, a co-organizer for the 2016 Asian American Engineer of the Year (AAEOY) award, and chair of the 2017 CIE-USA Centennial Planning Committee.